



PROFILE HIGHLIGHTS

- Founder & CEO – Kyros Semi
- 1-TOPS Coach
- Semiconductor Architect
- Ex-Intel Architect
- RISC-V Mentor
- US Patent Holder

TOP SKILLS

- Semiconductor Architecture
- RTL Design & Synthesis
- SoC & IP Development
- FPGA & Digital Design
- Leadership & Mentorship

PATENTS & RECOGNITION

- US Patent Holder
- US Patent Filed – 19/263,235
- Top 10 Women Leaders from BITS Pilani – 2025

Smitha B M V

Founder & CEO @ Kyros Semi | IP & SoC Digital Architect | 1-TOPS Coach | Mentor | Investor | Ex-Intel

A Semiconductor Technology Leader, Architect and Entrepreneur with nearly two decades of experience in IC Design, Semiconductor Product Development, SoC Architecture and RTL Design.

Professional Journey

Smitha is the Founder and CEO of **Kyros Semi Pvt Ltd**, focused on semiconductor innovation, IP development and product engineering solutions. Her vision is to move beyond traditional service models and position India as a global leader in semiconductor design.

She is also Co-Founder and Director at **Vivartan Technologies**, focused on bridging industry-academia gaps and building high-quality semiconductor engineering talent.

Prior to entrepreneurship, she spent over 7 years at **Intel Corporation**, where she worked as an IP Architecture Lead and Senior SoC Design Engineer leading conception, development and integration of IPs, SoCs and products.

Before Intel, she played a key role at **Saankhya Labs** contributing across architecture, RTL implementation and product validation.

Recognized among the **Top 10 Alumni Women Leaders from BITS Pilani – 2025** by Women Entrepreneurs Review magazine for her contributions to semiconductor innovation and entrepreneurship.

Leadership, Mentorship & Industry Contributions

- Coach in the **1-TOPS (1 Tape-Out Per Student)** program — a nationwide semiconductor initiative driven by the VLSI Society of India and supported by Government of India ecosystem initiatives.
- Mentors engineering teams and academic institutions in RISC-V architecture, SoC development and semiconductor product realization.
- Guided academic teams on RISC-V implementation from Architecture to GDSII realization.
- Served as Diversity & Inclusion Champion at Intel and contributed to workplace culture initiatives.

Professional Experience

Kyros Semi Pvt Ltd – Founder & CEO (Aug 2024 – Present)

Vivartan Technologies – Co-Founder & Director (May 2024 – Present)

Intel Corporation – IP Architecture Lead / Sr. SoC Design Engineer (2016 – 2024)

Saankhya Labs Pvt Ltd – SMTS (2009 – 2016)

Karmic – MTS / Trainee (2006 – 2008)

Educational Background

BITS Pilani – M.Tech in Microelectronics (2016 – 2018)

UBDT College of Engineering – BE in Electronics & Instrumentation Engineering (2002 – 2006)

Vision & Interests

Smitha strongly believes in innovation-led engineering and empowering teams to build impactful semiconductor technologies. Passionate about mentoring and indigenous semiconductor product development, she continues to contribute towards strengthening India's semiconductor ecosystem.