

Dr. C.P. Ravikumar  
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Bangalore 560093, INDIA

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# C.P. Ravikumar

## Experience

**Apr 2004-Current: Director of Technical Talent Development at TI, India**

**July 2008-July 2014**

**Director, University Relations, Texas Instruments, India (see address above). Also lead the TI India Technical University.**

**July 2001 – July 2008**

**Senior Technologist, Texas Instruments India, Bangalore (see address above). Also leads the TI India Technical University.**

**2000–2001                      Controlnet (India) Pvt Ltd                      Goa**  
**Vice President Training and R&D (On leave from IIT Delhi)**

**1991–2001                      Indian Institute of Technology                      Delhi**  
**Professor**

**1985–1985                      Processors Systems India                      Bangalore, India**  
**Systems Software Engineer**

**1983–1985                      Infosys Consultants Pvt Ltd. Bangalore, India**  
**Software Engineer**

## Education

1987–1991                      University of Southern California                      Los Angeles, CA

- Ph.D., Computer Engineering, Department of EE-Systems
- Thesis entitled “Parallel Algorithms for Automating Physical Design of VLSI Circuits.”

1985-1987                      Indian Institute of Science                      Bangalore, India

- Graduated with M.E. (Computer Science) *with highest CGPA* for the class
- M.E. Thesis entitled “Hardware Accelerators for VLSI Layout”

1978-1983                      U.V.C.E. (Bangalore                      Bangalore, India  
University)

- Graduated with a B.E. degree in Electronics. Winner of Gold Medal for highest marks for the University. Obtained first rank in 9 out of 10 semesters.

## Research Interests

I have published over 200 papers in areas related to VLSI Design Automation, Testing, Lower Power Test, and High-Performance Computing. I have guided 6 Ph.D. students, over 50 M.Tech projects, and over 50 B.Tech theses at IIT Delhi.

**Professional Contributions:** I organized the VLSI Design and Test Workshops (which later grew into VLSI Design and Test Symposium) from 1998 for 16 years. I have served on the Steering Committee of the International Conference on VLSI Design for 6 years. I have served on the program committees of several international and national conferences.

I have served on the editorial boards of the Journal of Electronic Testing (Kluwer), Journal of Low Power Electronics (still active on the board), IEEE Transactions on Circuits and Systems, Computer Science and Informatics (Computer Society of India), and International Journal of VLSI Design (Gordon and Breach). I am a Senior Member of IEEE.

## Summary

C.P. Ravikumar is the Director of Technical Talent Development at Texas Instruments, India. He is also the member of the global Learning Center of Excellence where he contributes to TI's global technical training needs. He is also an adjunct faculty at IIT Madras. Before joining TI India in 2001, Ravikumar was a Professor of Electrical Engineering at the Indian Institute of Technology, Delhi (1991-2001). He also held a visiting position at the University of Southern California (1995-1996) and the position of Vice President (Training) at Controlnet India Pvt Ltd (2000-2001). He obtained his Ph.D. (Computer Engineering) from the University of Southern California (1991), M.E. in Computer Science with highest scores from Indian Institute of Science (1987) and B.E. in Electronics with a Gold Medal from Bangalore University (1983).


He has published over 200 papers in leading International conferences and journals. He founded the VLSI Design and Test Symposium (VDAT) and has been the General Chair of this event from its inception in 1998. He is the author/editor/coauthor of over 12 books in areas of VLSI and has contributed several book chapters. He is the editor of the Journal of VLSI and Electronic System Design and on the editorial board of the Journal of Electronic Testing – Theory and Applications and the Journal of Low Power Electronics. He has won the best paper award at IEEE International Conference on VLSI Design (2002) and VLSI Test Symposium (2005). He is a Senior Member of IEEE, served as the honorary secretary of IEEE CAS Bangalore chapter which he founded for 16 years, and was the honorary secretary of VSI during 2003-2011.

## Publications, Patents, Professional Activities, and Awards

C.P. Ravikumar has authored over 200 technical papers in International journals and conferences. He is the author of the book "Parallel Methods for VLSI Layout" (Ablex Publishers, USA, 1995) and the editor of 7 books in the series "Progress in VLSI Design and Test" (Elite Publishing). He is the editor of the book "MSP430 Microcontroller in Embedded System Projects" (Elite Publishing, 2011).

He founded the VLSI Design and Test Workshops in 1998 and the VLSI Education Day in 2000 and served as the General Chair of these events since their inception until 2011 for 16 consecutive years. He is on the board of editors for the Journal of Low Power Electronics (American Scientific Publishers) since 2007 and has served on the board of editors for the Journal of Electronic Testing (Springer), IEEE Transactions on Circuits and Systems. He was also the honorary secretary of the VLSI Society of India during 2003-2011.

He was the co-editor of two special issues of the Journal of the Computer Society of India on the topics of Storage Area Networks and Computer Architecture. He has served as the Technical Program Chair of the International Conference on



VLSI Design (2001, 2002). He has served on the program committees of several conferences such as the International Conference on High Performance Computing and the International Conference on VLSI Design.

Ravikumar is a recipient of the ACM SIGDA Student Award at the IEEE/ACM Design Automation Conference (1990), the Best Student Paper award at the IEEE International Conference on VLSI Design (1998), the Best Paper Award at the IEEE International Conference on VLSI Design (2001), and the Best Paper Award at the VLSI Test Symposium (2005). He is a Fellow of the INAE (Indian National Academy of Engineering), a Senior member of the IEEE, a member of the IETE, and a fellow of the Indian Microelectronics Society. He holds 4 US patents (pending) in the area of VLSI Test. He is listed in the Marquis Who's Who in the World.

Ravikumar has 4 US patents in the area of VLSI Test. He received an award on behalf of Texas Instruments for the contributions made by the TI India University Program towards the development of Ecosystem (Universities) from Zinnov ("Zeal for Innovation").

**Website**

More detailed information about me, such as my publications, is available at my personal home page <http://cpravikumar.tripod.com>

## Creating Technical Content in Local Languages



Dr C.P. Ravikumar, who is an Adjunct Professor in IIIT Dharwad, is also a writer in Kannada language. He contributed a biweekly column called “*Janamukhi Tantraloka*” to the Kannada daily *Vijaya Karnataka* for about three years. The column carried articles pertaining to various domains such as Computer Science, Information Technology, Electronics and Engineering Education. He has a personal blog called “*Seepi Sampada*” where he writes on topics related to Science and Technology. Dr Ravikumar contributes to a number of Science periodicals published in Kannada language, such as *Kutuhali*. He has been invited by *Karnataka Science and Technology Academy* to deliver lectures in Kannada language on topics such as Data Science. He moderated a panel on the topic of Science Communication in Kannada at the annual conference of KSTA held in Dharwad (2019). He has served as member of advisors for *Kannada Pradhikara* (2020-2022). He has also recorded a number of videos in Kannada language on the topic of Python programming. It is Ravikumar’s belief that learning and thinking in one’s own mother tongue is very important to an individual’s intellectual growth. Dr Ravikumar has recently contributed a number of articles to Kannada Wikipedia on topics related to Computer Science and Electronics.

Generative AI	<a href="#">ಜೆನ್‌ಏಐ - ಸ್ವಪ್ನಿಶ್ಚಿಲ ಏಐ - ವಿಕಿಪೀಡಿಯ (wikipedia.org)</a>
Layered Architecture	<a href="#">ಪದರ ವಿನ್ಯಾಸ - ಲೇಯರ್ಡ್ ಆರ್ಕಿಟೆಕ್ಚರ್ - ವಿಕಿಪೀಡಿಯ (wikipedia.org)</a>
OEM	<a href="#">ಓಇಎಂ ಕಂಪನಿಗಳು - ವಿಕಿಪೀಡಿಯ (wikipedia.org)</a>
ODM	<a href="#">ಓಡಿಎಂ - ವಿಕಿಪೀಡಿಯ (wikipedia.org)</a>
LLM	<a href="#">ಎಲ್‌ಎಲ್‌ಎಮ್ - ವಿಕಿಪೀಡಿಯ (wikipedia.org)</a>
Python Programming Language	<a href="#">ಪೈಥಾನ್ ಪ್ರೋಗ್ರಾಮಿಂಗ್ ಭಾಷೆ - ವಿಕಿಪೀಡಿಯ (wikipedia.org)</a>
Quantum Computing	<a href="#">ಕ್ವಾಂಟಮ್ ಕಂಪ್ಯೂಟಿಂಗ್ - ವಿಕಿಪೀಡಿಯ (wikipedia.org)</a>
Gallium Nitride	<a href="#">ಗ್ಯಾಲಿಯಂ ನೈಟ್ರೈಡ್ - ವಿಕಿಪೀಡಿಯ (wikipedia.org)</a>
Silicon Carbide	<a href="#">ಸಿಲಿಕಾನ್ ಕಾರ್ಬೈಡ್ - ವಿಕಿಪೀಡಿಯ (wikipedia.org)</a>
Integrated Circuit	<a href="#">ಇಂಟೆಗ್ರೇಟೆಡ್ ಸರ್ಕ್ಯೂಟ್ - ವಿಕಿಪೀಡಿಯ (wikipedia.org)</a>
Linearity	<a href="#">ರೇಖೀಯತೆ - ವಿಕಿಪೀಡಿಯ (wikipedia.org)</a>
LiFi	<a href="#">ಲೈಫೈ - ವಿಕಿಪೀಡಿಯ (wikipedia.org)</a>